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| Attorney Docket No.                      | P-2966-US   |
| First Inventor or Application Identifier | BARAK, Ilan   |
| Title                                    | RF POWER AMPLIFIER AND METHODS FOR IMPROVING THE EFFICIENCY THEREOF |
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## APPLICATION ELEMENTS

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  - Brief Summary of the Invention
  - Brief Description of the Drawings (if filed)
  - Detailed Description
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## RF POWER AMPLIFIER AND METHODS FOR IMPROVING THE EFFICIENCY THEREOF

### BACKGROUND OF THE INVENTION

5       A typical radio transmitter uses a radio frequency (RF) power amplifier to amplify outbound signals for transmission by an antenna. A linear power amplifier receives a signal at an input power and produces the signal at an output power, which is the input power amplified by a gain. The linear power amplifier is driven by a direct current (DC) input voltage, provided for example  
10 by a battery in the transmitter, and the efficiency of the power amplifier is given by the ratio of the output power to the DC input power. RF power amplifiers are generally designed to provide maximum efficiency at the maximal output power. When the power amplifier produces an output power that is less than the maximal output power, the efficiency of the power amplifier may be significantly  
15 reduced.

      There are several situations where the output power of an RF transmitter needs to be less than the maximal output power ("power control"). For example, in amplitude modification (AM) radio transmission, the instantaneous output power is set according to the instantaneous strength of  
20 the modulating audio to be transmitted. In the case of battery-operated portable handsets such as cellular telephones and pagers, the power amplifier is one of the main current consumption elements, and power control is used in order to prolong the lifetime of the battery. Power control is also used to reduce interference between different calls in the same vicinity and to reduce the

radiation emitted by the handset. In fact, modern cellular systems allow handset transmission at low power for long periods of time.

However, since the efficiency of the power amplifier is significantly reduced when the power amplifier produces an output power that is less than  
5 the maximal output power, more battery power at lower output power is actually consumed. It would therefore be beneficial to improve the efficiency of the RF power amplifier at lower output powers without reducing the efficiency at the maximal output power.

## BRIEF DESCRIPTION OF THE DRAWINGS

The present invention will be understood and appreciated more fully from the following detailed description taken in conjunction with the appended drawings in which:

5        Figs. 1A and 1B are a schematic illustration of an exemplary radio frequency (RF) transmitter including an exemplary power amplifier, according to an embodiment of the invention;

      Fig. 2A is a graphical illustration of the efficiency  $\eta$  of the RF transmitter of Figs. 1A and 1B as a function of the normalized output power;

10       Fig. 2B is a graphical illustration of the efficiency  $\eta$  of the RF transmitter of Figs. 1A and 1B as a function of the normalized output power when the phase shifts have discrete values;

      Fig. 3 is a schematic illustration of another exemplary RF transmitter including another exemplary power amplifier, according to an embodiment of  
15    the invention;

      Fig. 4 is a schematic illustration of a further exemplary power amplifier, according to an embodiment of the invention; and

      Fig. 5 is a graphical illustration of the efficiency  $\eta$  of the RF transmitter of Figs. 1A and 1B as a function of the normalized output power, when bias  
20    control is performed at two output powers, according to an embodiment of the invention.

## DETAILED DESCRIPTION OF THE PRESENT INVENTION

Embodiments of the present invention are directed to a radio frequency (RF) power amplifier. Embodiments of the present invention are also directed to a method for controlling the output power of a radio frequency amplifier.

5 Up-conversion from baseband frequencies to radio frequencies involves an up-conversion chain, which includes a chain of amplifiers and preamplifiers interspersed with modulators, mixers and bandpass filters. Reference is now made to Figs. 1A and 1B, which are a schematic illustration of an exemplary radio frequency (RF) transmitter including an exemplary power  
10 amplifier, according to an embodiment of the present invention. A baseband modulation signal 100 may be provided to an intermediate frequency (IF) modulator 102. IF modulator 102 may receive an IF local oscillator (LO) signal from an IF local oscillator (LO) 104. IF modulator 102 may modulate the IF LO signal with baseband modulation signal 100, thus producing an IF modulated  
15 signal 106. IF modulated signal 106 may be provided to an IF amplifier 108, which may amplify it by a gain  $\gamma_{IF}$ , thus producing an amplified IF modulated signal 110. A mixer 112 may receive an RF LO signal from an RF local oscillator 114. Mixer 112 may mix the RF LO signal with signal 110, thus producing an RF signal 116. RF signal 116 may be filtered by a bandpass filter  
20 118, and then provided to an RF preamplifier 120, which may amplify it by a gain  $\gamma_{RF}$  thus producing an RF signal 122. RF signal 122 may be provided to a power amplifier 124, which may amplify it to an output power for transmission by an antenna 126. Either IF amplifier 108 or RF preamplifier 120, or both, have a variable gain ( $\gamma_{IF}$  and/or  $\gamma_{RF}$ ) that can be controlled by power amplifier

124 according to the desired value of the output power. The controller of the radio transmitter may provide the desired value of the output power to power amplifier 124, as indicated by arrow 127.

Power amplifier 124 is shown in detail in Fig. 1B. Power amplifier 124  
5 comprises an outphasing system 128 with shunt reactance and a logic controller 130. The input to outphasing system 128 is RF signal 122. The amplitude of RF signal 122 may be determined by many factors, among them the modulation and the gain of IF amplifier 108 and RF preamplifier 120.

Outphasing systems with shunt reactance (commonly known as Chireix  
10 outphasing systems) are known in the art. An example is described in F. Raab, "Efficiency of Outphasing RF Power-Amplifier Systems", IEEE Transactions on Communications, vol. COM-33, No. 10, Oct. 1985. The Chireix outphasing system was designed in 1935 to improve the efficiency and linearity of AM broadcast transmitters. It comprises a transmission-line-coupler with shunt  
15 reactance. An outphasing system with shunt reactance achieves maximal efficiency for two values of the output power.

Outphasing system 128 comprises two branch amplifiers 132 and 134 connected in parallel and a splitter 136 for splitting RF signal 122, so that a signal with half the input power is provided to branch amplifier 132 and a signal  
20 with half the input power is provided to branch amplifier 134. Branch amplifiers 132 and 134 are driven by a DC input voltage  $V_{DC}$ . Outphasing system 128 also comprises phase shifters 138 and 140 for modifying the phase of RF signal 122 provided to branch amplifiers 132 and 134, respectively. Outphasing system 128 may include shunt reactance elements 142 and 144 at the output  
25 of branch amplifiers 132 and 134, respectively.  $B_s$  is the shunt reactance of

element 142 and  $-B_s$  is the shunt reactance of element 144. Through proper selection of the shunt reactance  $B_s$ , the efficiency may be maximized at a specific output power. The efficiency of a specific output power may be improved by the adjusting of the shunt reactance  $B_s$ .

5        Outphasing system 128 may also comprise a transmission-line-coupler 146 for combining the outputs of branch amplifiers 132 and 134. Transmission-line-coupler 146 comprises two transmission lines 148 and 150, each of which serves as an impedance inverter connected to antenna 126 so that the sum of the branch currents goes through the load. Other combiner  
10       schemes yielding the same performance could be implemented instead, namely Hybrid BALUN or center tap inductor.

Logic controller 130 controls the output power of power amplifier 124 according to the desired value of the output power. Logic controller 130 controls the output power of the power amplifier by utilizing two methods of  
15       power control. For a first range of output powers, for example 15 dB, logic controller 130 performs outphasing. Logic controller 130 may vary the phase of outphasing system 128 in order that outphasing system 128 provides antenna 126 with a signal at an output power according to the desired value. For a second range of output powers, logic controller 130 varies the amplitude of RF  
20       signal 122, which is the input to outphasing system 128. The operation of logic controller 130 is better understood if reference is made additionally to Figs. 2A and 2B.

Fig. 2A is a graphical illustration of the efficiency  $\eta_s$  of the RF transmitter of Figs. 1A and 1B as a function of the normalized output power.  
25       The outphasing system with shunt reactance has a peak efficiency at P1, and a

peak efficiency at P2. When the desired output power is at or greater than a threshold, namely P1, logic controller 130 performs outphasing, setting the phase  $\phi_m$  so that outphasing system 128 applies  $+\phi_m$  to phase shifter 138 and  $-\phi_m$  to phase shifter 140, thus producing an output power according to the  
s desired value. It can be seen that when the output power is at or greater than P1, the efficiency of the RF transmitter of Figs. 1A and 1B is the same as that of a pure Chireix outphasing system.

For the range of output powers below threshold P1, logic controller 130 keeps the phase  $\phi_m$  constant at  $\phi_{m-MAX}$  and reduces the gain ( $\gamma_{IF}$  and/or  $\gamma_{RF}$ )  
10 of IF amplifier 108 or RF preamplifier 120 or both, thus reducing the amplitude of RF signal 122, which is the input to outphasing system 128. It can be seen that at the range of output powers below threshold P1, the efficiency of the RF transmitter of Figs. 1A and 1B is significantly improved with respect to both the class-B power amplifiers and a pure Chireix outphasing system.

15 It will be appreciated by persons of ordinary skill in the art that IF amplifier 108 could be replaced by a series of IF amplifiers, at least one of which has variable gain which is controlled by logic controller 130 when the desired output power is lower than P1. Similarly, it will be appreciated that RF preamplifier 120 could be replaced by a series of RF preamplifiers, at least one  
20 of which has variable gain which is controlled by logic controller 130 when the desired output power is lower than P1. In general, any serial combination of IF amplifiers prior to mixer 112 and any serial combination of RF preamplifiers prior to power amplifier 124 could be used, provided that at least one of the IF amplifiers and RF preamplifiers has variable gain which is controlled by logic  
25 controller 130 when the desired output power is lower than P1.



In another example of the present invention, the phase shifts  $+\phi_m$  and  $-\phi_m$  provided by the logic controller to the phase shifters do not vary continuously between  $\phi_{m-MIN}$  and  $\phi_{m-MAX}$ , but set the phase to a value from a collection of discrete phase values. Fig. 2B is a graphical illustration of the efficiency  $\eta$  of the RF transmitter of Figs. 1A and 1B as a function of the normalized output power when the phase shifts have discrete values.

Reference is now made to Fig. 3, which is a schematic illustration of another exemplary RF transmitter including an exemplary power amplifier, according to an embodiment of the present invention. A baseband modulation signal 300 may be provided to an RF modulator 313. RF modulator 313 may receive an RF LO signal from an RF local oscillator 314. RF modulator 313 may modulate the RF LO signal with baseband modulation signal 300, thus producing an RF signal 316. RF signal 316 may be filtered by a bandpass filter 318, and then may be provided to a chain of at least one RF preamplifier 320, which may amplify it by a gain  $\gamma_{RF}$  thus producing an RF signal 322. RF signal 322 may be provided to a power amplifier 324, which may amplify it to an output power for transmission by an antenna 326. At least one of the RF preamplifiers 320 may have a variable gain ( $\gamma_{RF}$ ) that is controlled by power amplifier 324 according to the desired value of the output power. The controller of the radio transmitter may provide the desired value of the output power to power amplifier 324, as indicated by arrow 327.

Power amplifier 324 may comprise an outphasing system 328 with shunt reactance and a logic controller 330. The input to outphasing system 328 is RF signal 322. The amplitude of RF signal 322 is determined by many factors, among them the modulation and the gain of RF preamplifier 320.

Outphasing system 328 may be the same as or similar to outphasing system 128 of Fig. 1B and will not be described further.

Logic controller 330 may control the output power of power amplifier 324 according to the desired value of the output power. Referring again briefly to Fig. 2A, when the desired value of the output power is at or greater than a threshold, namely P1, logic controller 330 performs outphasing, setting the phase  $\phi_m$  so that outphasing system 328 provides antenna 126 with a signal at an output power according to the desired value. It can be seen that when the output power is at or greater than P1, the efficiency of the RF transmitter of Fig. 3 is the same as that of a pure Chireix outphasing system. For desired output powers lower than P1, logic controller 330 may keep the phase  $\phi_m$  constant at  $\phi_{m-MAX}$  and varies the gain ( $\gamma_{RF}$ ) of at least one of the variable gain RF preamplifiers 320, thus reducing the amplitude of RF signal 322, which is the input to outphasing system 328. It can be seen that at output powers lower than threshold P1, the efficiency of the RF transmitter of Fig. 3 is significantly improved with respect to both the class-B power amplifiers and a pure Chireix outphasing system.

According to another aspect of the present invention, the logic controller sets the gain of at least one of the IF amplifiers and the RF preamplifiers even when the desired value of the output power is at or above threshold P1 in order to improve the efficiency of the outphasing system 128 and/or 328.

Reference is now made to Fig. 4, which is a schematic illustration of a further exemplary power amplifier, according to an embodiment of the invention. A power amplifier 424 receives an input RF signal 422 and amplifies

it for transmission by an antenna 426. Power amplifier 424 comprises an outphasing system 428 with shunt reactance and a logic controller 430.

Outphasing system 428 comprises two branch amplifiers 432 and 434 connected in parallel and a splitter 436 for splitting RF signal 422, so that a  
5 signal with half the input power is provided to branch amplifier 432 and a signal with half the input power is provided to branch amplifier 434. Branch amplifiers 132 and 134 are driven by a DC input voltage  $V_{DC}$ . Outphasing system 428 also comprises phase shifters 438 and 440 for modifying the phase of RF signal 422 provided to branch amplifiers 432 and 434, respectively. Outphasing  
10 system 428 includes shunt reactance elements 442 and 444 at the output of branch amplifiers 432 and 434, respectively.  $B_s$  is the shunt reactance of element 442 and  $-B_s$  is the shunt reactance of element 444. Through proper selection of the shunt reactance  $B_s$ , the efficiency may be improved at a specific output power.

15 Outphasing system 428 also comprises a transmission-line-coupler 446 for combining the outputs of branch amplifiers 432 and 434. Transmission-line-coupler 446 comprises two transmission lines 448 and 450, each of which serves as an impedance inverter connected to antenna 426 so that the sum of the branch currents goes through the load. Other combiner  
20 schemes yielding the same performance could be implemented instead, namely Hybrid BALUN or center tap inductor.

Outphasing system 428 is similar to outphasing system 128 of Fig. 1B, with a difference that outphasing system 428 further comprises at least one RF preamplifier 400 between splitter 436 and phase shifter 438 and at least one  
25 RF preamplifier 402 between splitter 436 and phase shifter 440. At least one of

RF preamplifier 400 may have a variable gain  $\gamma_{RF}^{(1)}$  and at least one of RF preamplifier 402 has variable gain  $\gamma_{RF}^{(2)}$ . Logic controller 430 sets the gain for each of RF preamplifier 400 and RF preamplifier 402 separately in order to improve the efficiency of outphasing system 428.

5        Power amplifiers may have a bias current in order to preserve its linearity. The consumption of bias current may decrease the efficiency of the power amplifier. At high output powers, this decrease in efficiency may be relatively unimportant. However, at low output powers, the loss in efficiency is significant. Bias control is the act of adjusting the bias current in order to  
10    increase the efficiency of the power amplifier, and is known in the art.

      According to another aspect of the present invention, the internal bias current of a power amplifier comprising an outphasing system with shunt reactance may be reduced at low output powers, where the power amplifier may be more linear. This may affect the linearity of the power amplifier, but it  
15    may also improve the efficiency.

      Reference is now made to Fig. 5, which is a graphical illustration of the efficiency  $\eta$  of the RF transmitter of Figs. 1A and 1B as a function of the normalized output power, when bias control is performed. Bias control is performed at two output powers, indicated by peaks 500 and 502. By  
20    comparing the graph of Fig. 5 with that of Fig. 2A, it can be seen that bias control may improve the efficiency of the RF transmitter of Figs. 1A and 1B at low output powers.

      As explained hereinabove, the branch amplifiers of an outphasing system with shunt reactance are driven by a direct current (DC) input voltage,  
25    provided for example by a battery in the transmitter. It is known in the art that at

low output powers, reducing the DC input voltage improves the efficiency of the branch amplifiers without significantly damaging the linearity. By reducing the supply voltage of the branch amplifiers 132 and 134 of Fig. 1B and 432 and 434 of Fig. 4, the efficiency of the outphasing system is improved at the output  
5 power range below P1.

According to another aspect of the present invention, the DC input voltage of the branch amplifiers for an outphasing system with shunt reactance is reduced at the output power range below P1.

It will be appreciated by persons of ordinary skill in the art that the  
10 present invention is not limited by what has been particularly shown and described herein above. Rather the scope of the invention is defined by the claims that follow:

What is claimed is:

1. A method comprising:

varying the power of an input signal to an outphasing system at a first range of output power values; and

- 5 adjusting a phase of said outphasing system at a second range of output power values.

2. The method of claim 1, wherein said outphasing system has shunt reactance.

3. A method comprising:

- 10 varying the power of an input signal to a power amplifier when a desired output power is below a threshold; and

performing outphasing when said desired output power is at said threshold and above said threshold.

4. The method of claim 3, wherein said power amplifier includes an outphasing system with shunt reactance having a variable phase, said varying comprises:

setting said variable phase to a fixed value; and

reducing the power of the input signal.

5. The method of claim 4, wherein said outphasing system with shunt reactance has a peak efficiency at an upper power at a first value of said variable phase and at a lower power at a second value of said variable phase, wherein said threshold is said lower power and said fixed value is said second value.

6. The method of claim 4, wherein the outphasing system is operably coupled to at least one of a radio frequency (RF) preamplifier and

to an intermediate frequency (IF) amplifier and said varying comprises:

lowering a gain of at least one of said RF preamplifier and said IF amplifier.

5 7. The method of claim 4, wherein said variable phase is a collection of discrete phase values.

8. The method of claim 7, wherein said outphasing system with shunt reactance has a peak efficiency at an upper power at a first value of said variable phase and at a lower power at a second value of said variable phase, wherein said threshold is said lower power and said fixed value is said second value.

10

9. The method of claim 7, wherein said outphasing system is operably coupled to at least one of a radio frequency (RF) preamplifier and to an intermediate frequency (IF) amplifier and said varying comprises:

15

lowering a gain of at least one of said RF preamplifier and said IF amplifier.

10. A method comprising:

providing a first method of power control for a desired output power at a first range of power values which is below a threshold; and

20

providing a second method of power control for a desired output power at a second range of power values which is above or at said threshold.

11. The method of claim 10, wherein said first method is reducing the power of an input signal to said power amplifier and said second method is outphasing.
12. The method of claim 11, wherein said outphasing is setting at  
5 least one phase value from a collection of discrete phase values.
13. The method of claim 10, wherein said first method is reducing the power of an input signal to said power amplifier and said second method is outphasing and reducing the power of the input signal.
14. The method of claim 12, wherein said outphasing is setting at  
10 least one phase value from a collection of discrete phase values.
15. A method comprising:  
modifying the amplitude of an input signal to an outphasing system with shunt reactance.
16. The method of claim 15, wherein said outphasing system is  
15 operably coupled to at least one of a radio frequency (RF) preamplifier and to an intermediate frequency (IF) amplifier and said modifying comprises:  
adjusting a gain of at least one of said RF preamplifier and said IF amplifier.
- 20 17. A method comprising:  
modifying the amplitude of input to at least one of two branch amplifiers of an outphasing system with shunt reactance.
18. The method of claim 17, wherein a first of said two branch amplifiers is operably coupled to a first radio frequency preamplifier and a second of said two branch amplifiers is  
25



operably coupled to a second radio frequency preamplifier and  
said modifying comprises:

adjusting a gain of said first radio frequency preamplifier and  
adjusting a gain of said second radio frequency preamplifier.

5 19. A method comprising:

modifying the input to an outphasing system with shunt reactance;  
and

performing bias control in said outphasing system with shunt  
reactance.

10 20. The method of claim 19, wherein said performing comprises:

reducing an internal bias current of at least one branch amplifier in  
said outphasing system with shunt reactance.

21. The method of claim 19, wherein said performing comprises:

15 reducing a supply voltage of at least one branch amplifier in said  
outphasing system with shunt reactance.

22. A radio frequency (RF) power amplifier comprising:

a controller coupled to an outphasing system to provide a variation  
of the power of an input signal to said outphasing system at a first  
range of output power values and to provide an adjustment of a phase  
20 of the outphasing system at a second range of output power.

23. The RF power amplifier of claim 22, wherein said outphasing  
system further comprises a shunt reactance.

24. A radio frequency (RF) power amplifier comprising:

a controller coupled to an outphasing system to provide a variation  
25 of the power of an input signal to said outphasing system when a

desired output power is below a threshold and said controller performs outphasing when said desired output power is at said threshold and above said threshold.

25. The RF power amplifier of claim 24, wherein said outphasing  
5 system further comprises a shunt reactance and at least two  
phase shifters and wherein said controller is coupled to said  
phase shifters to provide each of said phase shifters with a phase  
value.
26. The RF power amplifier of claim 25, wherein said outphasing  
10 system has a peak efficiency at an upper power and at a lower  
power, wherein said threshold is said lower power.
27. The RF power amplifier of claim 25, wherein said outphasing  
system is operably coupled to at least one of a radio frequency  
(RF) preamplifier and to an intermediate frequency (IF) amplifier  
15 and said controller is coupled to at least one of said RF  
preamplifier and said IF amplifier to provide an adjustment of a  
gain of said at least one of said RF preamplifier and said IF  
amplifier.
28. The RF power amplifier of claim 24, wherein said outphasing  
20 system comprises at least two phase shifters and said controller is  
coupled to said at least two phase shifters to provide a collection  
of discrete phase values to said at least two phase shifters.
29. The RF power amplifier of claim 28, wherein said outphasing  
system comprises a shunt reactance and has a peak efficiency at

an upper power and at a lower power, wherein said threshold is said lower power.

30. The RF power amplifier of claim 28, wherein the outphasing system is operably coupled to at least one of a radio frequency (RF) preamplifier and to an intermediate frequency (IF) amplifier and said controller is coupled to at least one of said RF preamplifier and said IF amplifier to provide an adjustment of a gain of at least one of said RF preamplifier and IF amplifier.
31. A radio frequency (RF) power amplifier comprising:
- 10 a controller operably coupled to at least one of a radio frequency (RF) preamplifier and to an intermediate frequency (IF) amplifier and to an outphasing system with shunt reactance to provide a modification of the amplitude of an input signal to said outphasing system.
32. The RF power amplifier of claim 31, wherein said outphasing system with shunt reactance comprises:
- 15 a splitter operably coupled to at least two RF preamplifiers;
  - at least two phase shifters each operably coupled to a respective one of said at least two RF preamplifiers; and
  - at least two branch amplifiers each operably coupled to a respective one of said at least two phase shifters;
  - 20 wherein said controller is operably coupled to at least one of said at least two RF preamplifiers to provide a gain modification of said at least one of said at least two RF preamplifiers.
33. A radio frequency (RF) power amplifier comprising:

an outphasing system with shunt reactance operably coupled to a controller wherein said controller is coupled to said outphasing system to provide a modification of an input signal to said outphasing system and to perform bias control in said outphasing system.

5 34. The RF power amplifier of claim 33, wherein said outphasing system comprises at least one branch amplifier and said controller is coupled to said outphasing system to reduce an internal bias current of said at least one branch amplifier.

10 35. The RF power amplifier of claim 33, wherein said outphasing system comprises at least one branch amplifier and said controller is coupled to said outphasing system to reduce a supply voltage of said at least one branch amplifier.

## ABSTRACT

A radio frequency power amplifier characterized by an efficiency and operative to produce an output signal at a desired output power includes an outphasing system with shunt reactance and a controller. The outphasing  
5 system with shunt reactance has a variable phase and is operative to produce the output signal from an input signal. The controller is operative to control the variable phase and the amplitude of the input signal in order to achieve high efficiency at low desired output powers. A method for controlling the output  
10 power of a radio frequency power amplifier includes reducing the power of an input signal to the power amplifier when a desired output power is below a threshold and performing outphasing when the desired output power is not below the threshold.

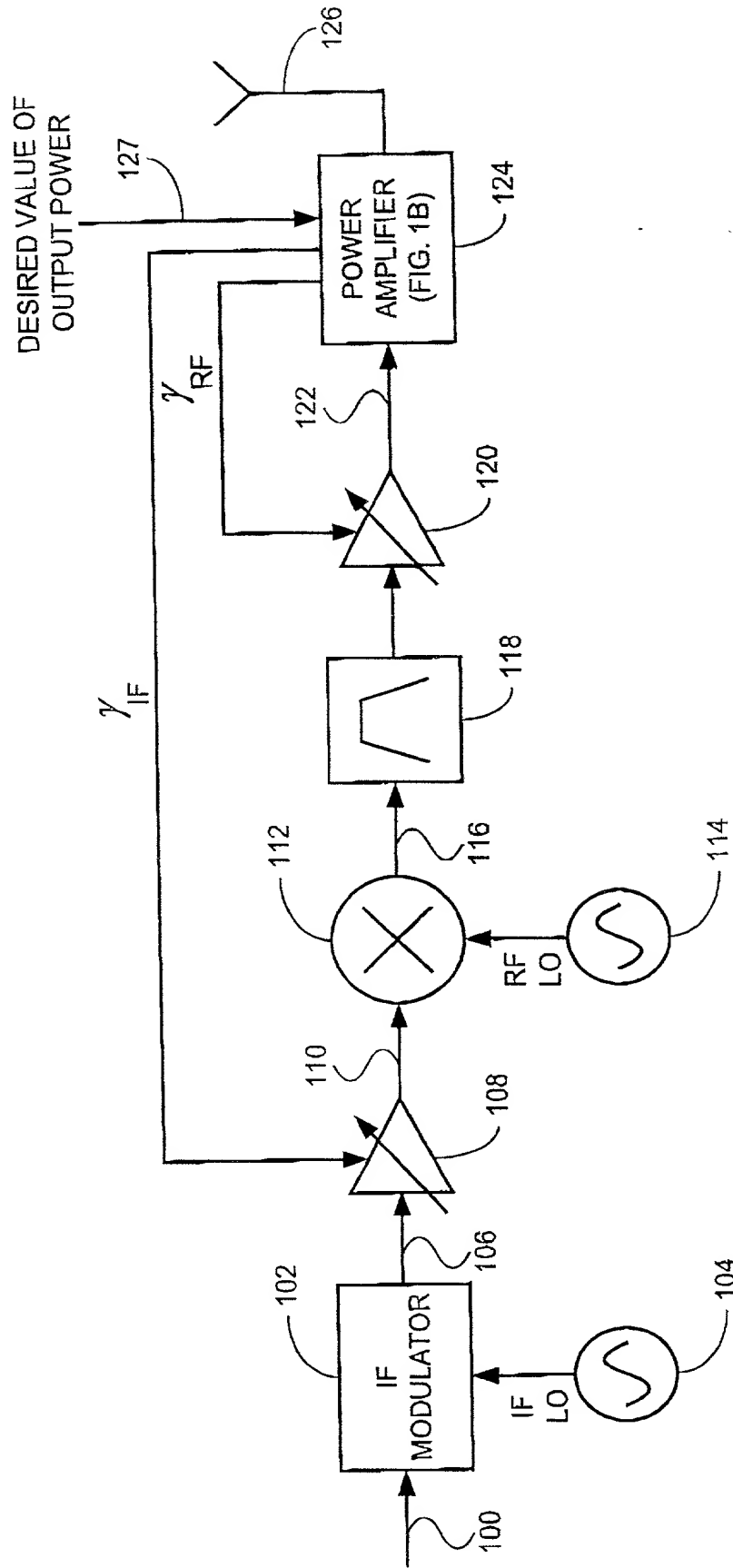


FIG. 1A

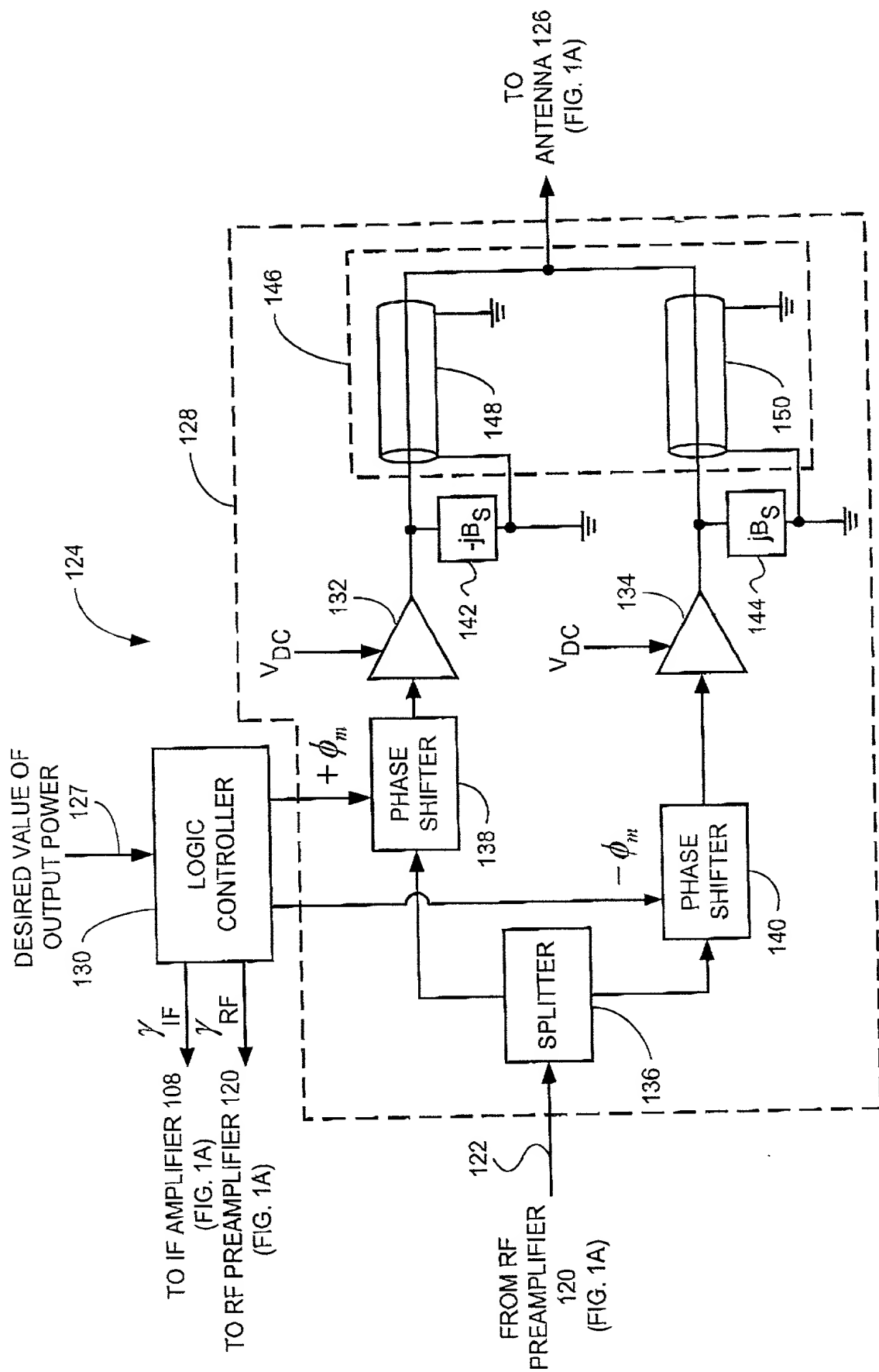


FIG. 1B

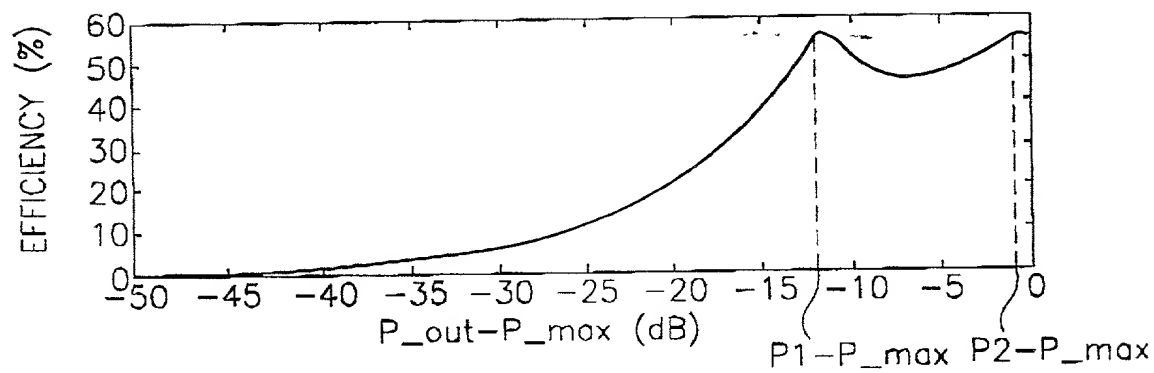


FIG. 2A

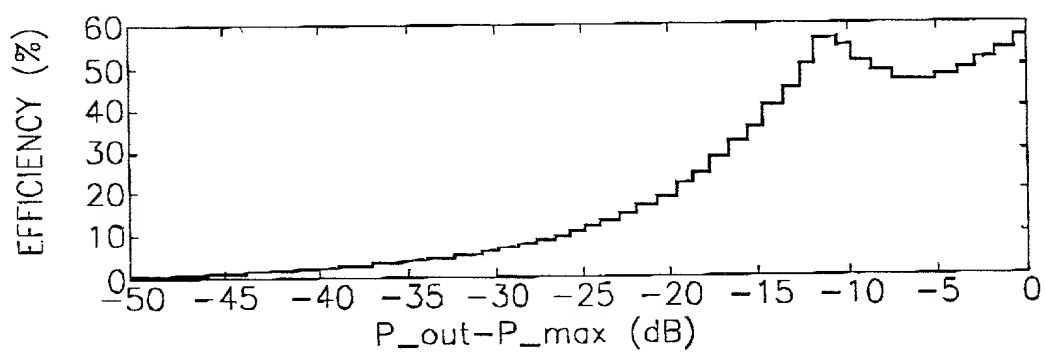


FIG. 2B



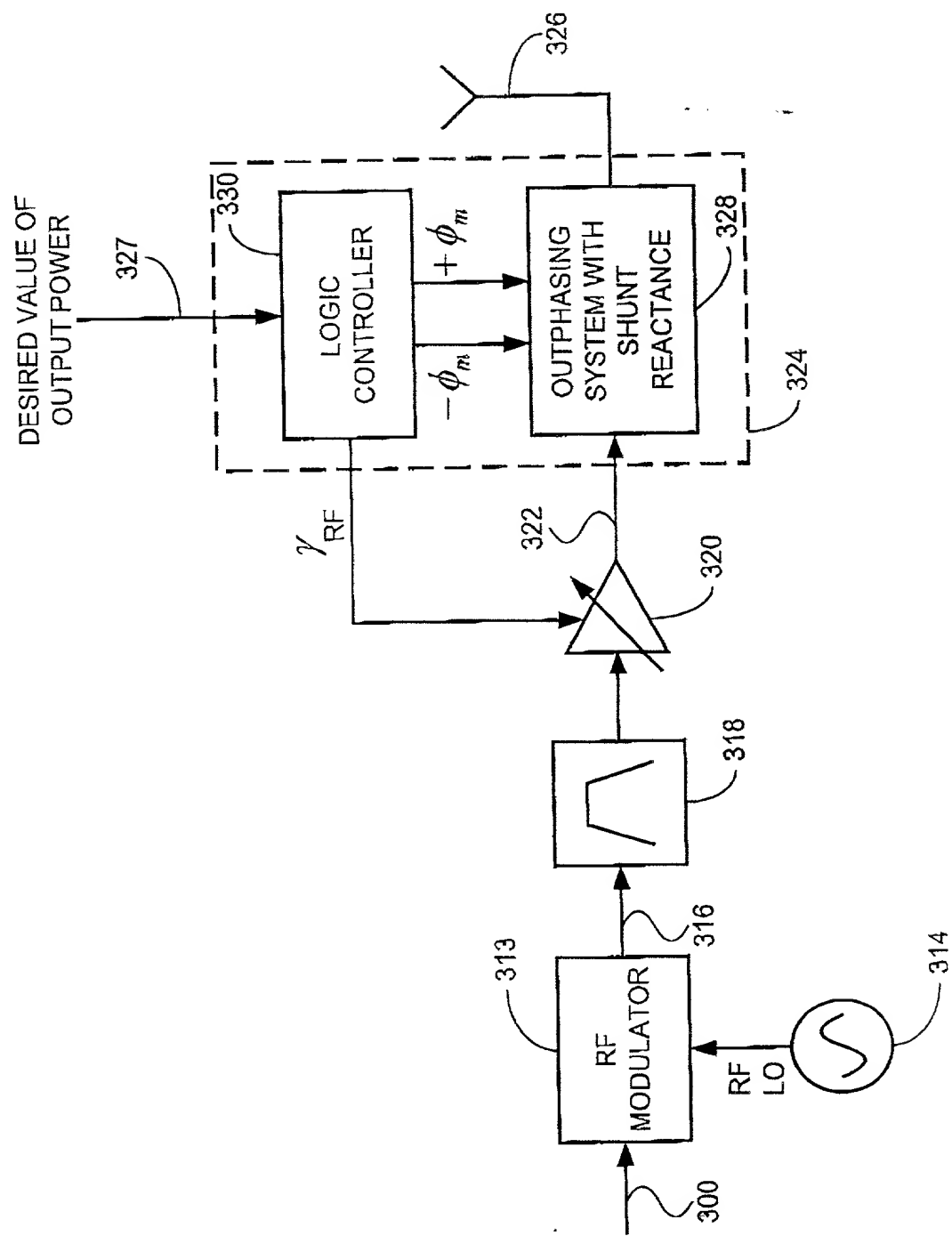


FIG. 3



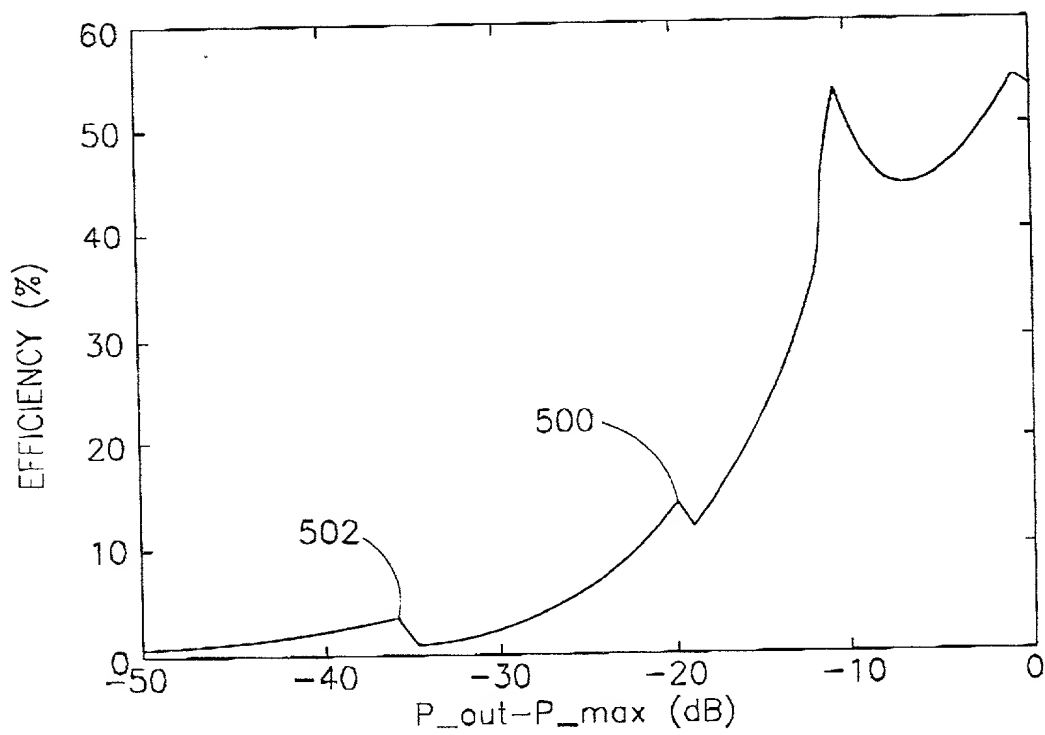


FIG.5

**DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below under my name.

I believe that I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled

**RF POWER AMPLIFIER AND METHODS FOR IMPROVING THE EFFICIENCY  
THEREOF**

the Specification of which

☒  
☐

is attached hereto

was filed on

as Application Serial No. \_\_\_\_\_

and was amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified Specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, 1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any provisional application filed in the United States in accordance with 35 U.S.C. §1.119(e), or any application for patent that has been converted to a Provisional Application within one (1) year of its filing date, or any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed.

**PRIOR FILED APPLICATION(S)**

| <u>APPLICATION</u><br><u>NUMBER</u> | <u>COUNTRY</u> | <u>(DAY/MONTH/YEAR FILED)</u> | <u>PRIORITY</u><br><u>CLAIMED</u> |
|-------------------------------------|----------------|-------------------------------|-----------------------------------|
|-------------------------------------|----------------|-------------------------------|-----------------------------------|

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application listed below, and, insofar as the subject matter of each of the claims of this application is not disclosed in any prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a), which occurred

between the filing date of the prior application and the national or PCT international filing date of this application:

APPLICATION  
NO. \_\_\_\_\_

FILING DATE  
(DAY/MONTH/YEAR) \_\_\_\_\_

STATUS - PATENTED,  
PENDING, ABANDONED \_\_\_\_\_

I hereby appoint as my attorney(s) and agent(s) Heidi M. Brun (Agent, Registration No. 35,104), or Jerome R. Smith, Jr. (Attorney, Registration No. 35,684), or Daniel J. Swirsky (Agent, Registration No. 45, 148) or Mark S. Cohen (Attorney, Registration No. 42, 425) said attorney(s) and agent(s) with full power of substitution and revocation to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further, that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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DATE \_\_\_\_\_

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DATE \_\_\_\_\_